

Please amend the paragraph beginning at page 3, line 7 as follows:

92 A couple of conventional methods of analyzing the waveform of a source current are referenced. A first method includes expanding the digital circuits to a transistor level and using a circuit simulator for transition analysis thus to examine the waveform of the source current. A second method includes approximating the waveform of a consumed current at each logic gate in the digital circuits to a triangle wave. The triangle wave represents that the charging and discharging processes of load impedance in the switching operation of the logic gate complete within switching time. Then, the waveforms of the currents of the digital circuits are summed to have a waveform of the source current (K. Shimazaki, H. Tsujikawa, S. Kojima, and S. Hirano, "LEMINGS: LSI's EMI-Noise Analysis with Gate Level Simulator", the proceedings of IEEE, ISQED2000).

IN THE CLAIMS:

93 2. (Amended) The method according to claim 1, wherein where the digital circuit is divided into a plurality of segments along the border at which the parasitic impedances of the source line and the ground line are locally increased, the parasitic capacitor series and the group of the parasitic capacitors statically charged are assigned for a group of the logic gates included in each segment.

94 9. (Amended) The apparatus according to claim 8, wherein where the digital circuit is divided into a plurality of segments along the border at which the parasitic impedances of the source line and the ground line are locally increased, the